

# デュアルミラー光学系用の カメラモジュールの開発と性能評価

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# The Cherenkov Telescope Array (CTA) Observatory

LST × 3-5  
D ~ 23 m  
FOV ~ 4-5°  
Pix ~ 0.1°  
E ~ 0.02-5 TeV

MST × 15-25  
D ~ 12 m  
FOV ~ 7-8°  
Pix ~ 0.18°  
E ~ 0.1-30 TeV

SC-MST × 36  
D = 9 m  
FOV = 8°  
Pix = 0.07°  
E ~ 0.1-10 TeV

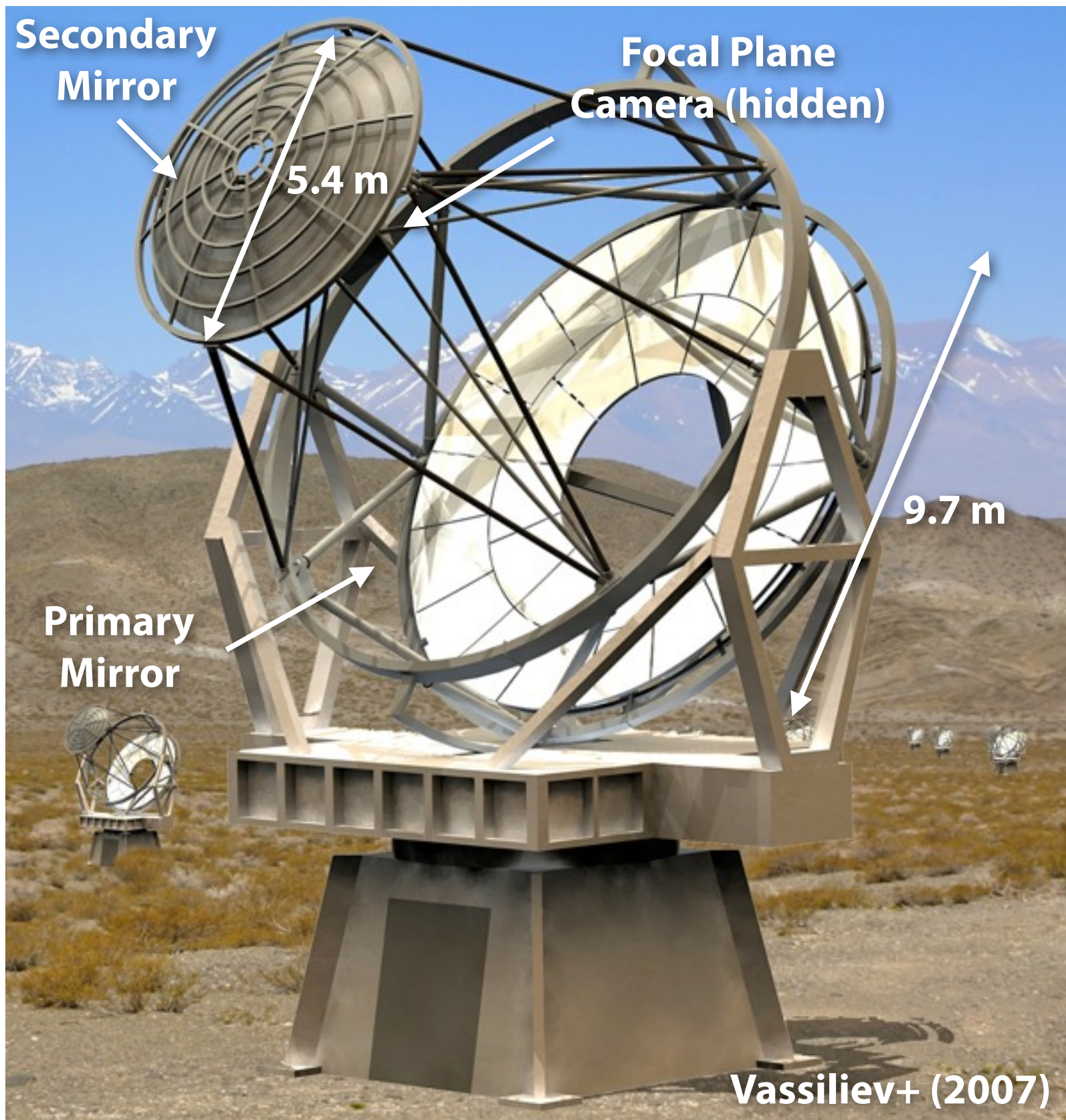
SST × 25-100  
D ~ 4-7 m  
FOV ~ 7-10°  
Pix ~ 0.2-0.25°  
E ~ 1-300 TeV



- ❖ The next-generation imaging atmospheric Cherenkov telescope (IACT)
- ❖ Consists of Large/Medium/Small Size Telescopes (LSTs/MSTs/SSTs)
- ❖ Expansion with Schwarzschild-Couder MSTs by CTA-US



# The Schwarzschild-Couder Optical System

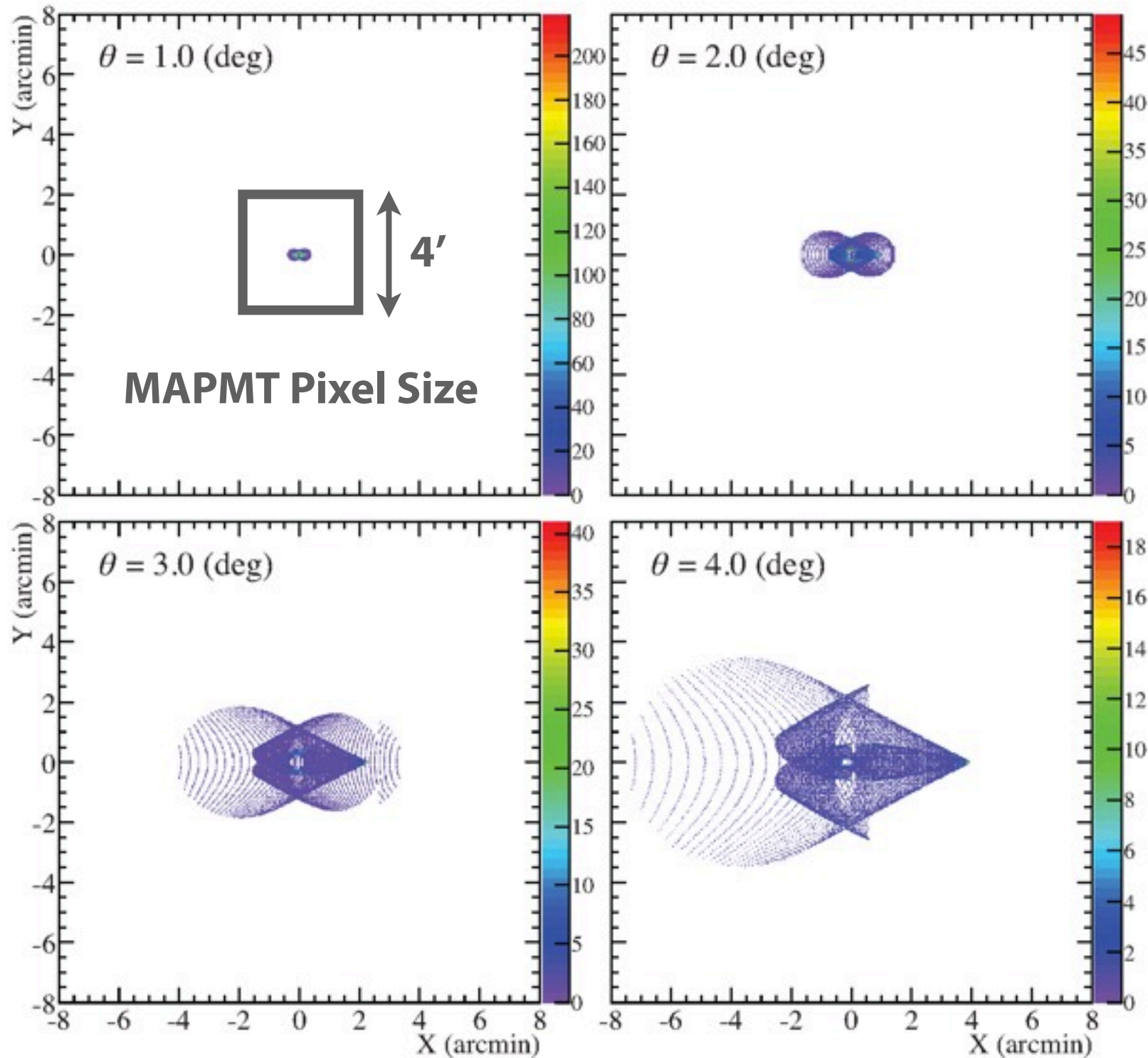


- ❖ Originally planned for AGIS (→ CTA-US)
- ❖ Fairly large mirror size with diameter of **9.7 m**
- ❖ Wide FOV of **8°**
- ❖ Small angular resolution of **< 4'**
- ❖ MAPMT array consisting of more than **11,000 channels**
- ❖ The first challenge of a dual-mirror system in CR experiments



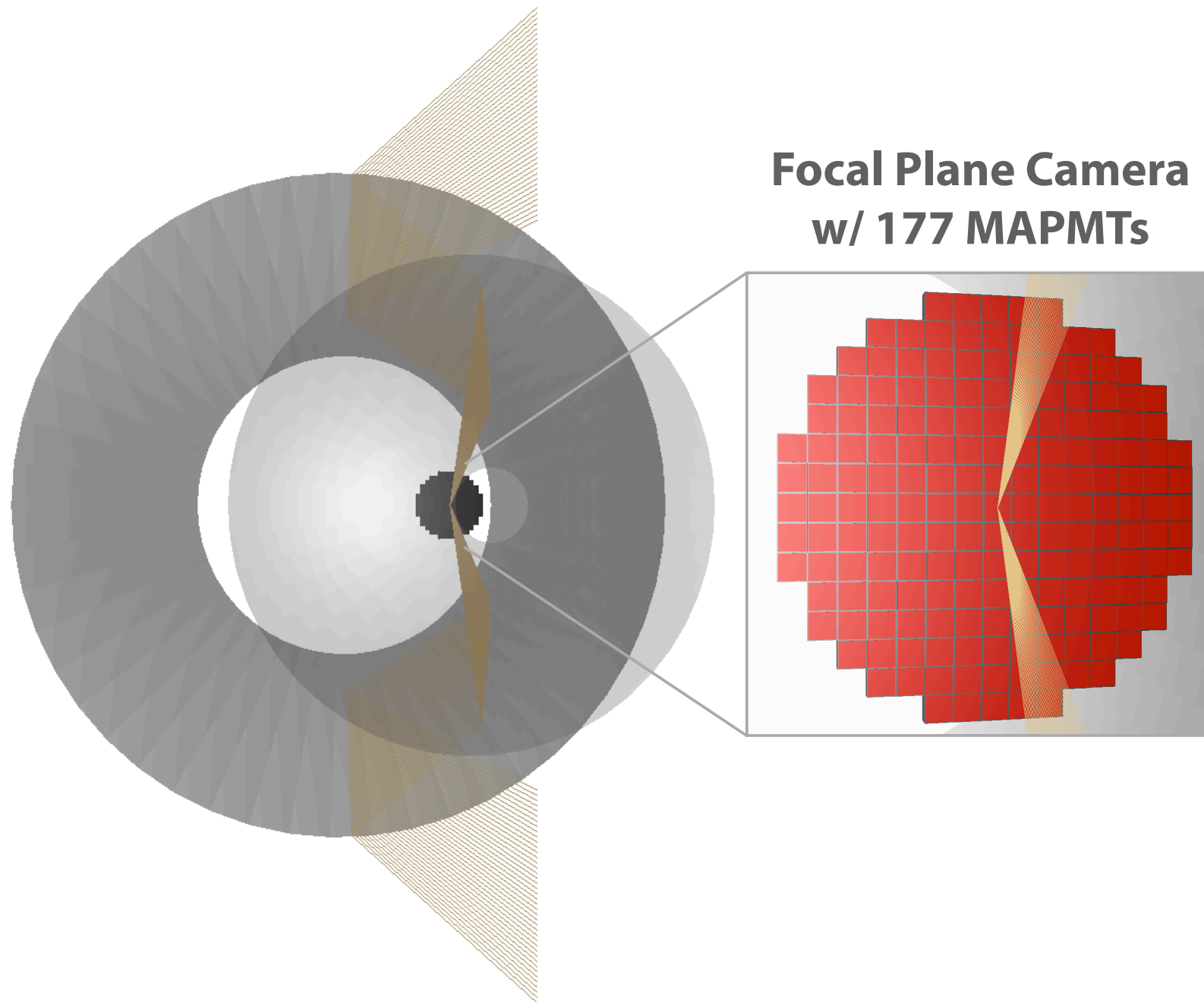
# The Schwarzschild-Coude Optical System

Spot Diagrams



- ❖ Originally planned for AGIS ( $\rightarrow$  CTA-US)
- ❖ Fairly large mirror size with diameter of **9.7 m**
- ❖ Wide FOV of  **$8^\circ$**
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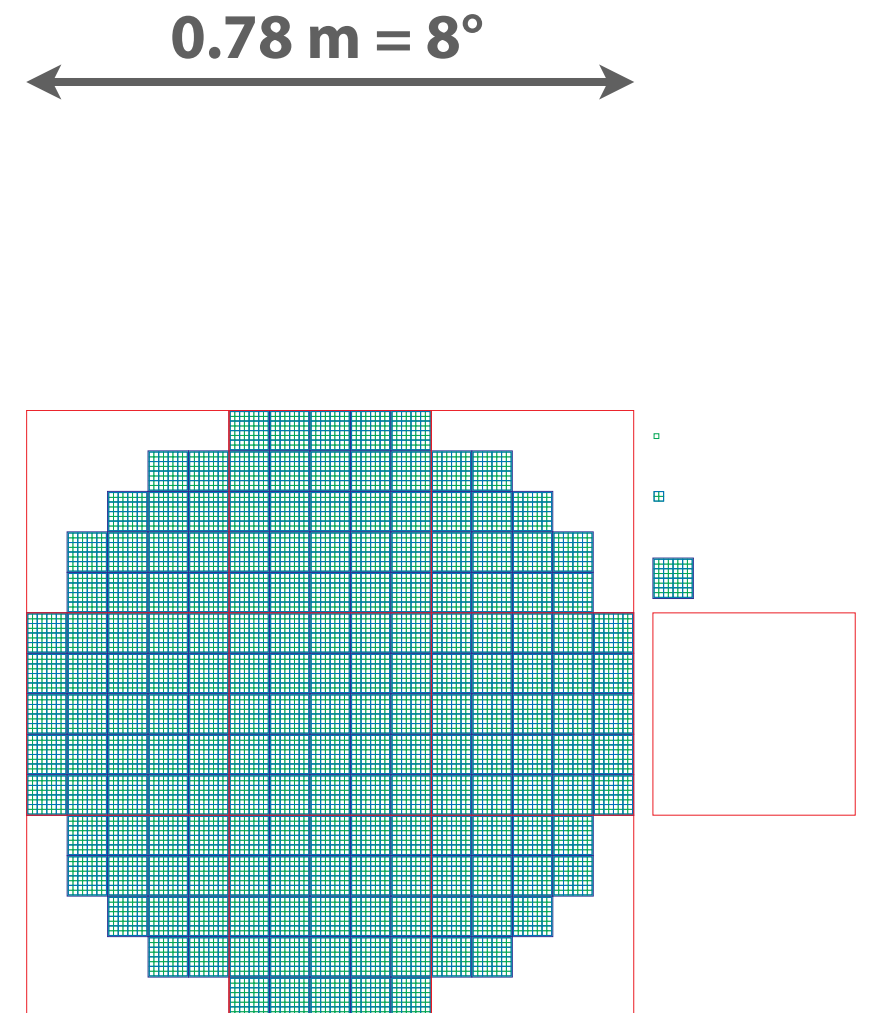
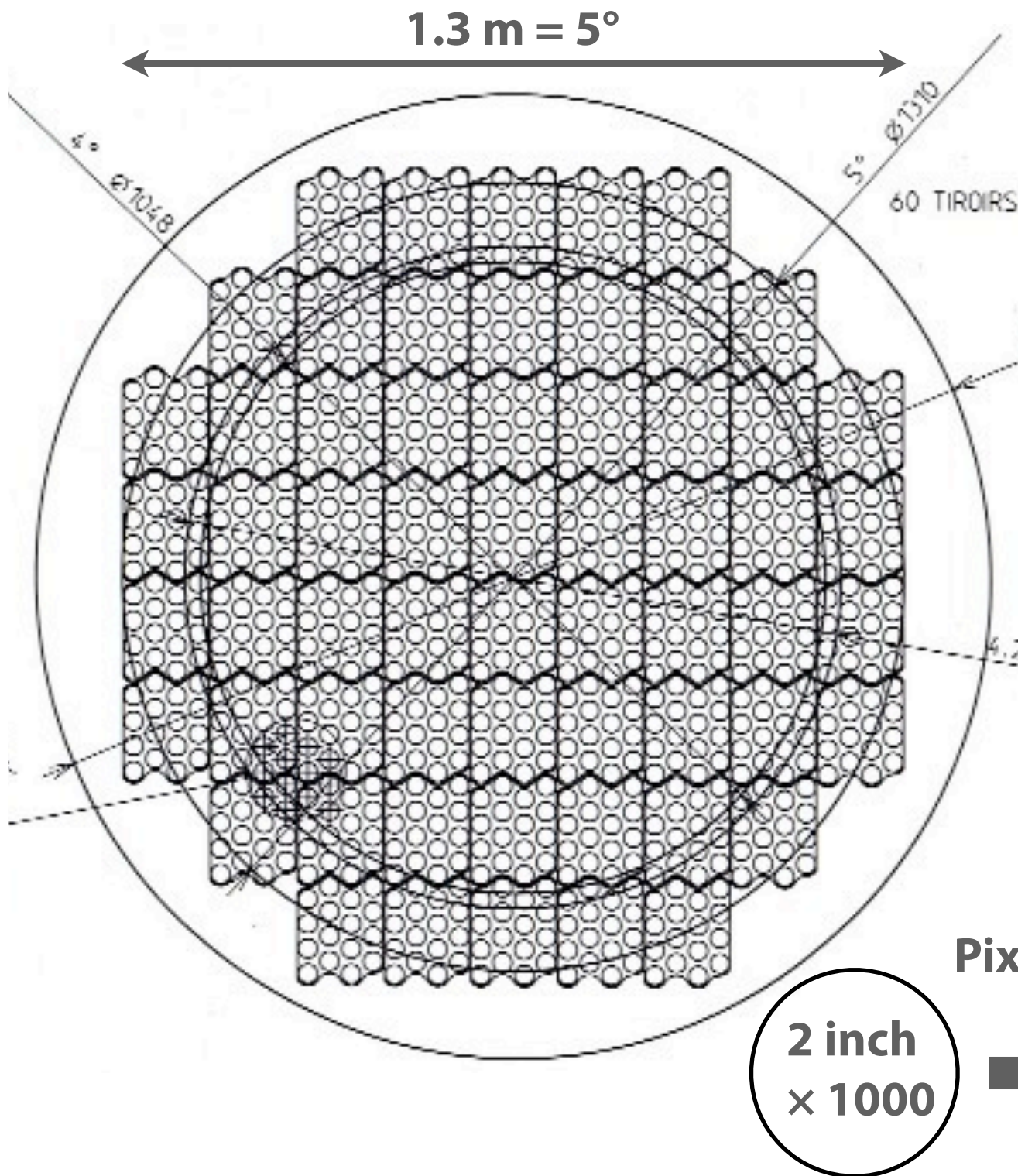
# The Schwarzschild-Coude Optical System



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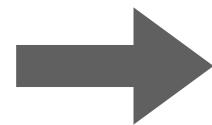


# Need a Very Compact Front-end Electronics with Low \$/ch



Pixel Size

2 inch  
× 1000



6 mm  
× 10000

HESS Camera w/ Regular PMTs

SC Optics Camera w/ MAPMTs



# The TARGET Camera Module

TeV Array with Gsa/s sampling and Experimental Trigger

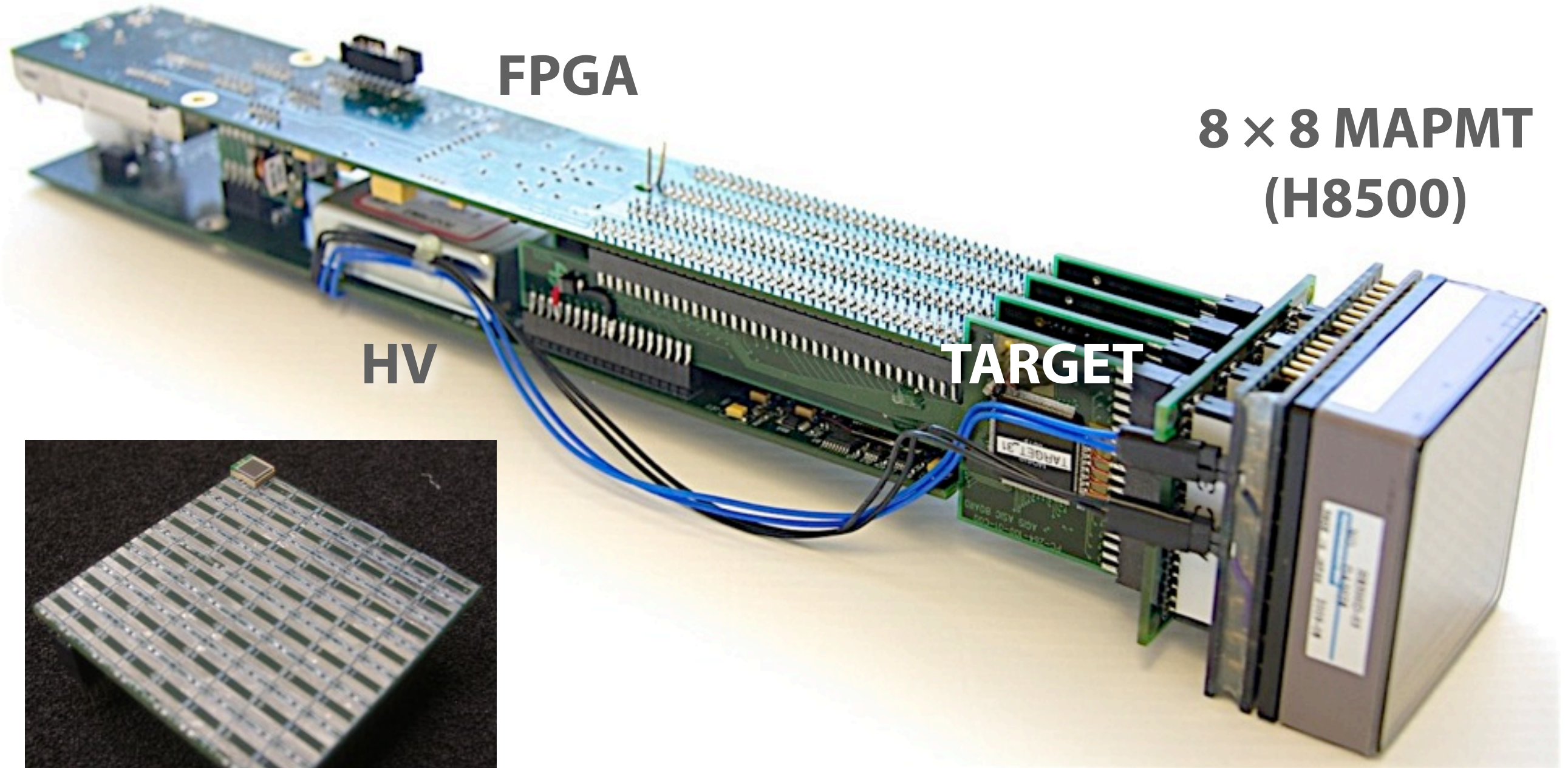
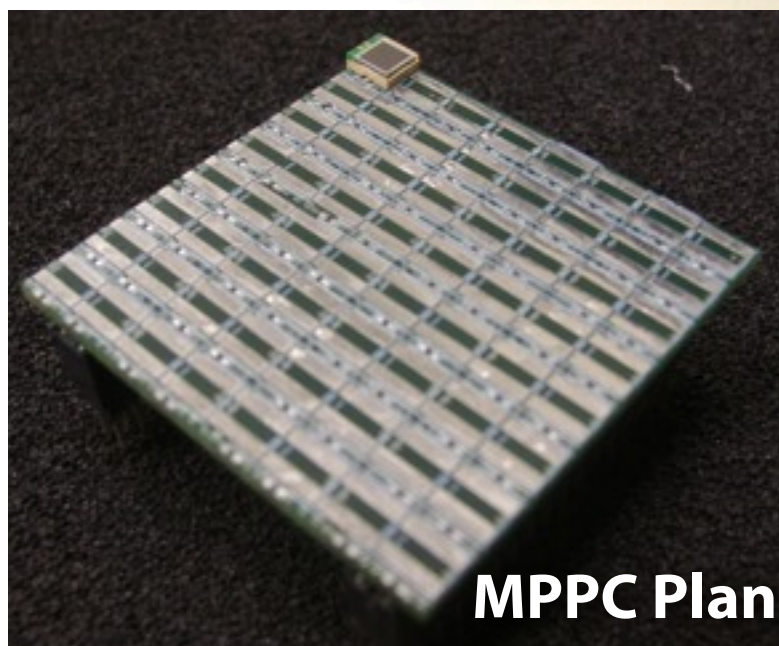
Fiber USB

FPGA

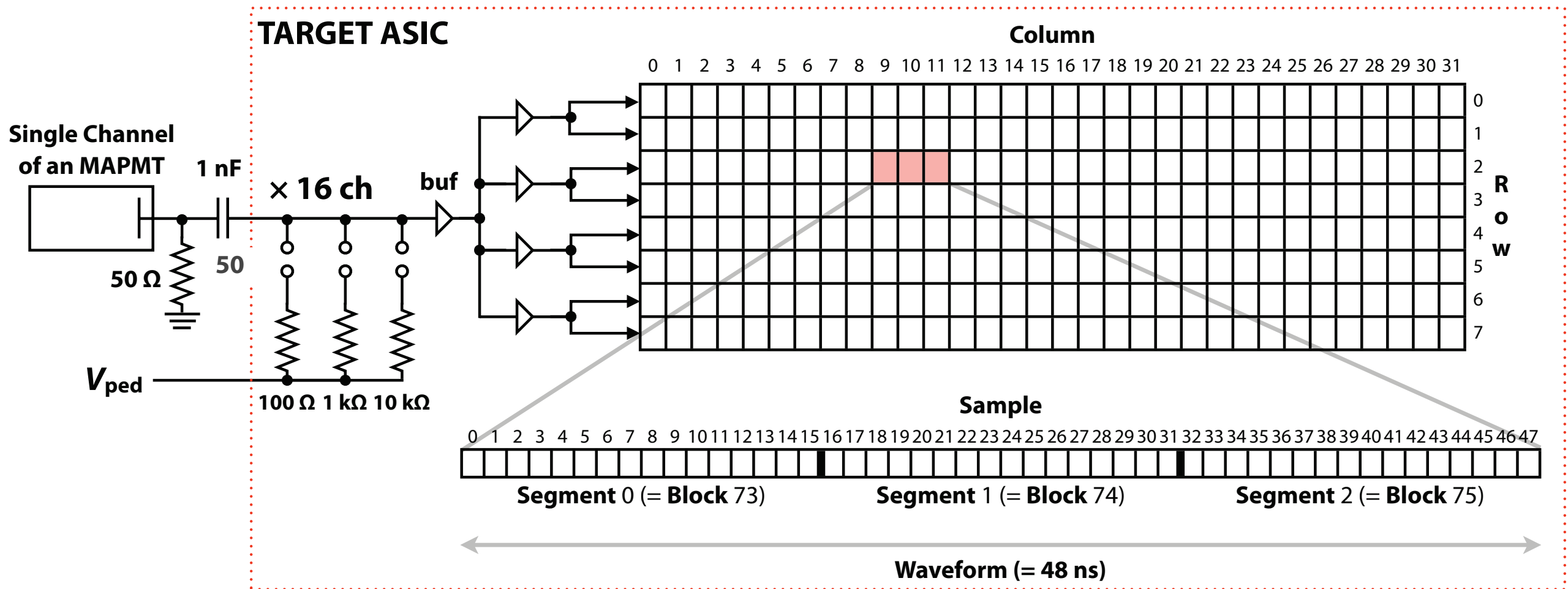
8 × 8 MAPMT  
(H8500)

HV

TARGET



# TARGET Concept

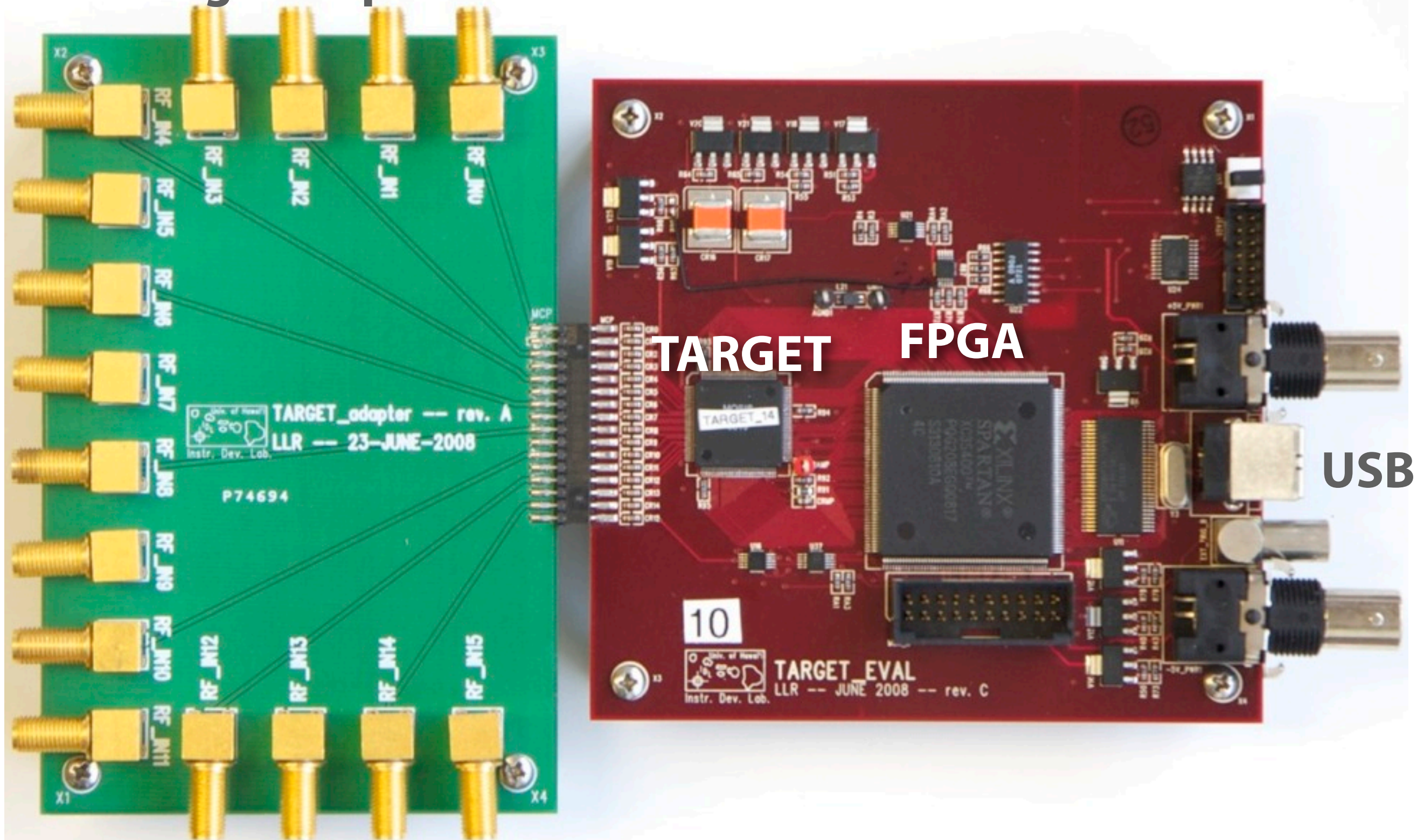


- ❑ **16-ch** integrated system × **4096 capacitors** (= 4- $\mu$ s buffer at 1 GHz)
- ❑ **Self trigger, 1 GHz or faster** sampling speed
- ❑ **9 or 10-bits** dynamic range
- ❑ Low cost (~\$20/ch), low power (~5W/64ch)

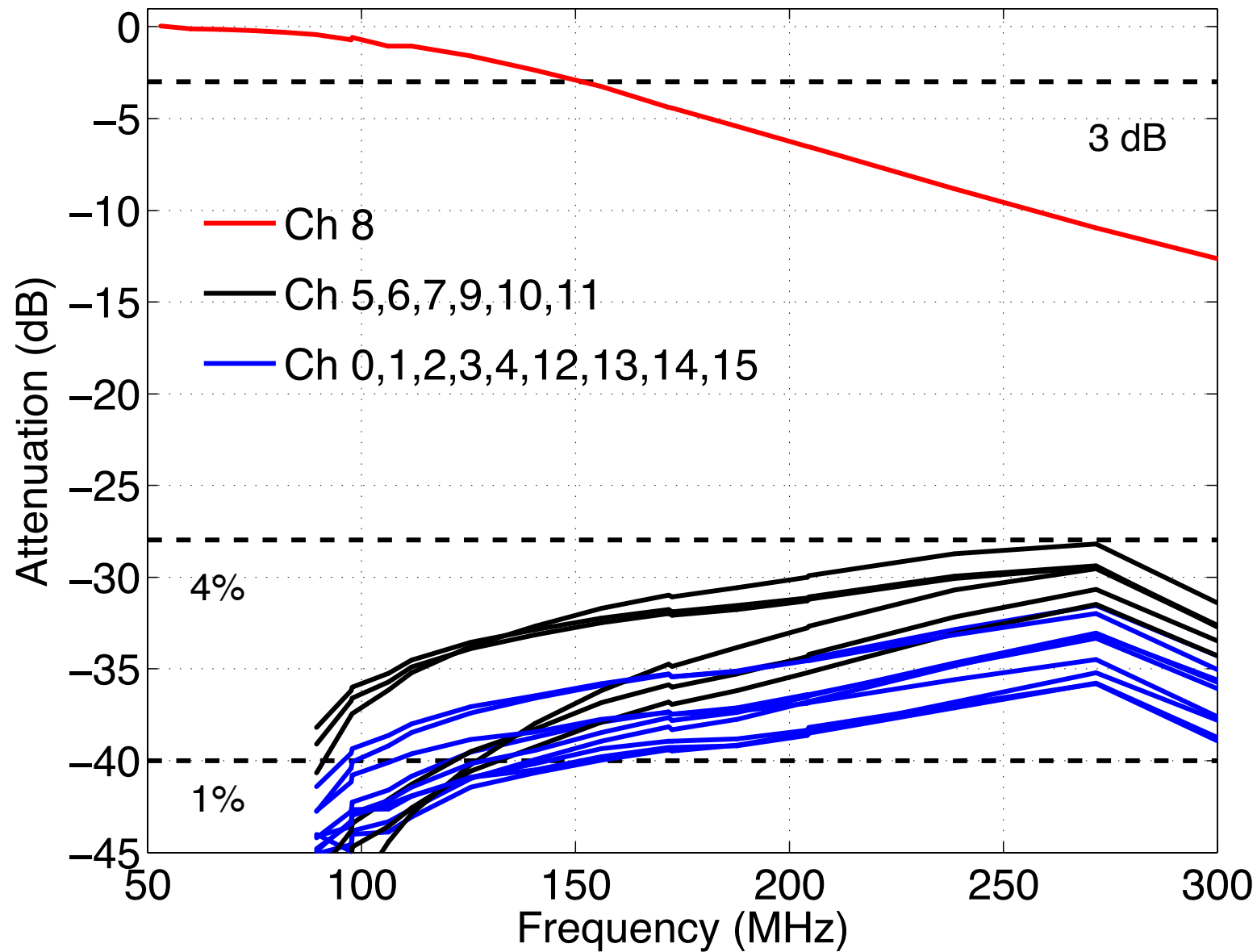


# TARGET 1 Evaluation Board

## Signal Inputs



# Bandwidth and Cross Talk



- Bandwidth and cross talk were measured with sinusoid input
- Bandwidth  $\sim 150$  MHz at 3 dB  $\rightarrow$   $>380$  MHz in TARGET 2
- Cross talk  $\sim 4\%$  at 300 MHz  $\rightarrow$   $< 1\%$  in TARGET 2



# The TARGET Camera Module

TeV Array with Gsa/s sampling and Experimental Trigger

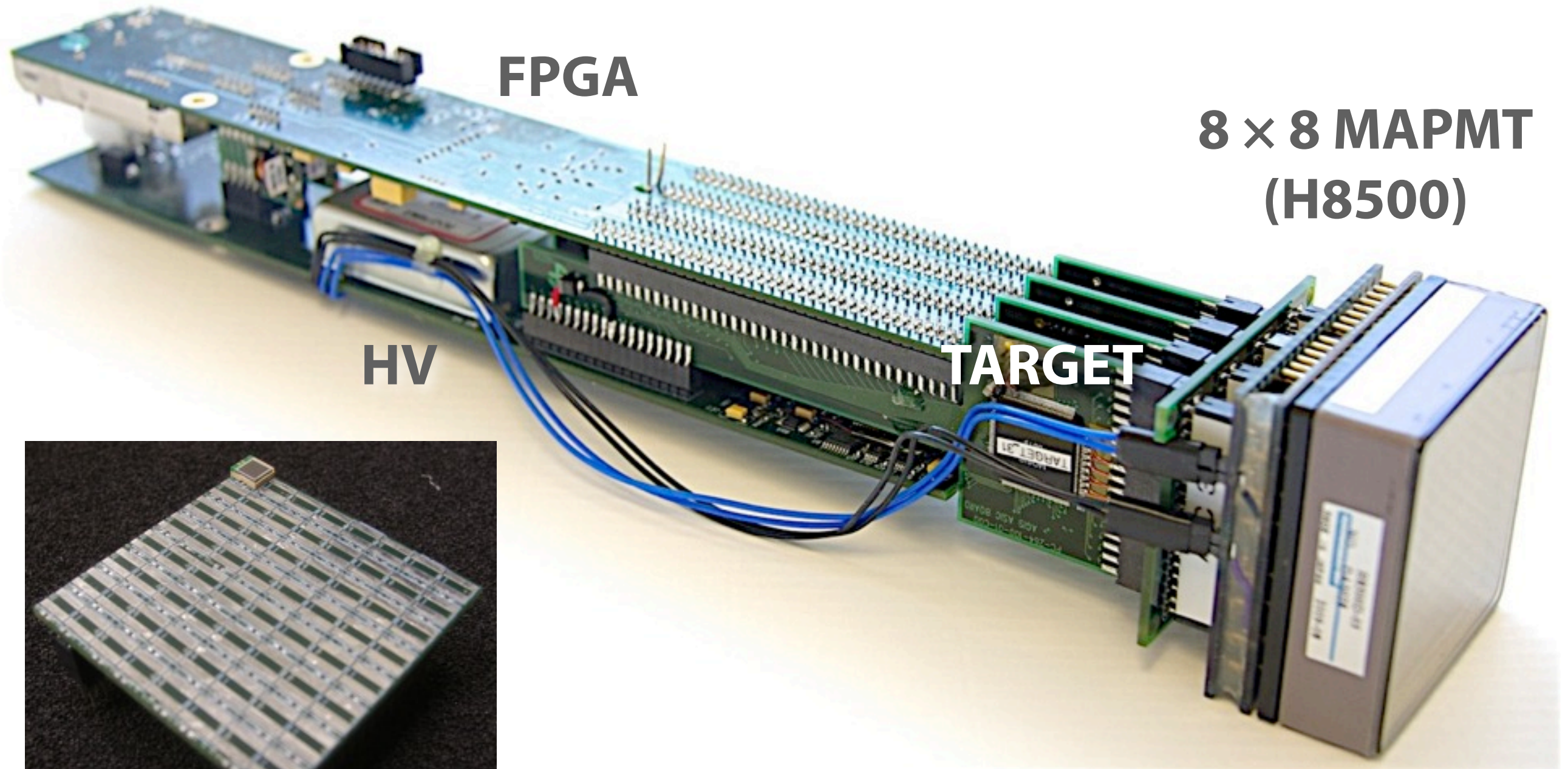
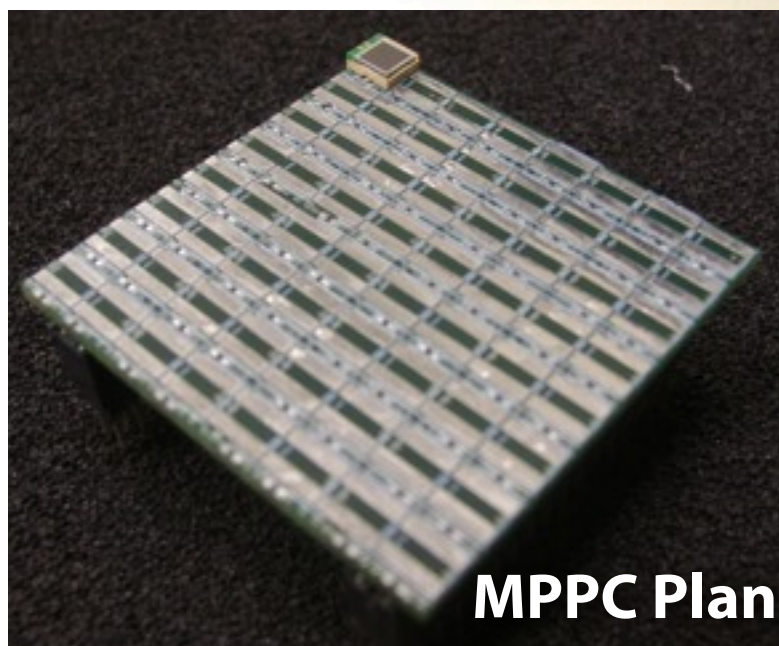
Fiber USB

FPGA

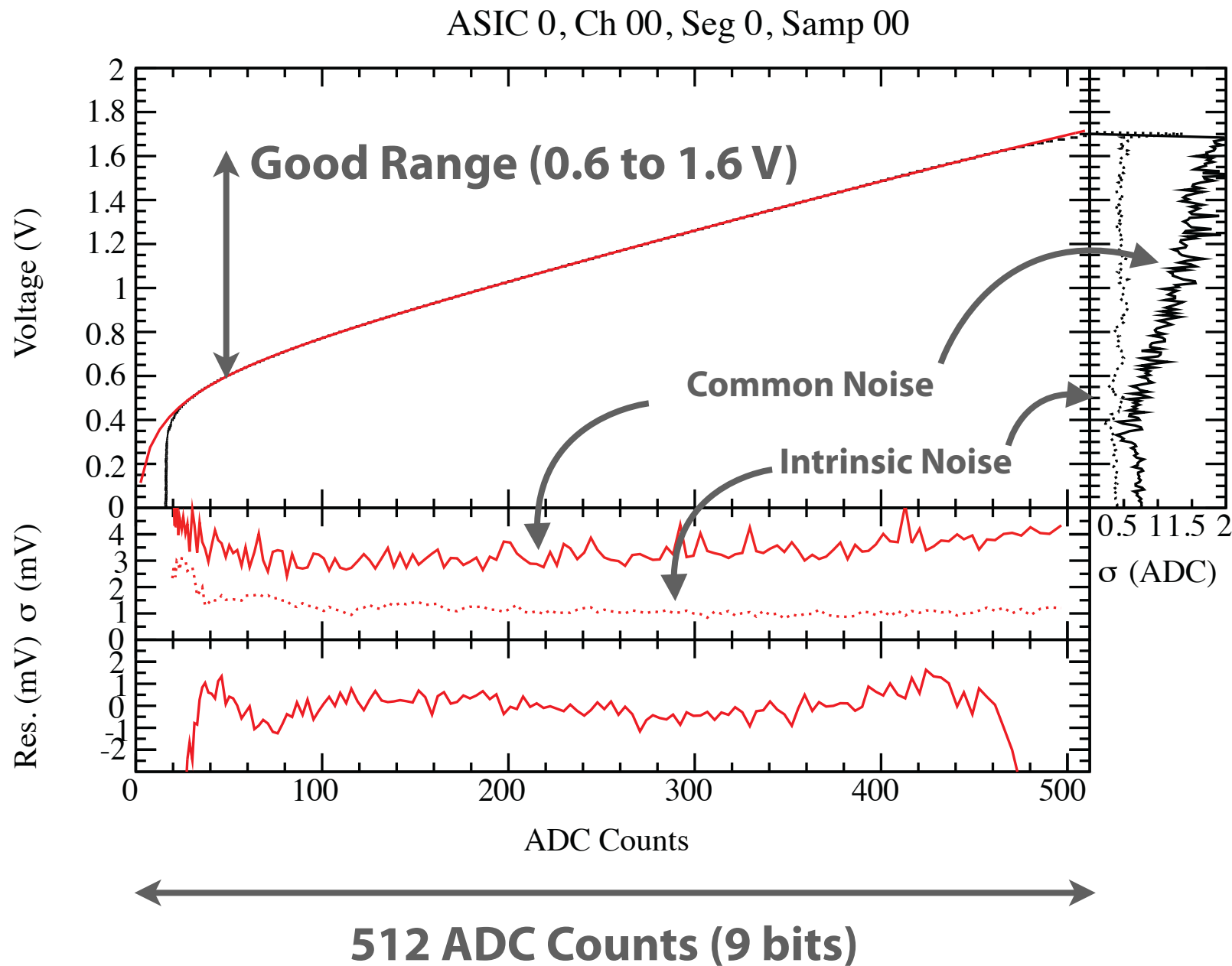
8 × 8 MAPMT  
(H8500)

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TARGET



# Calibration of the Transfer Function

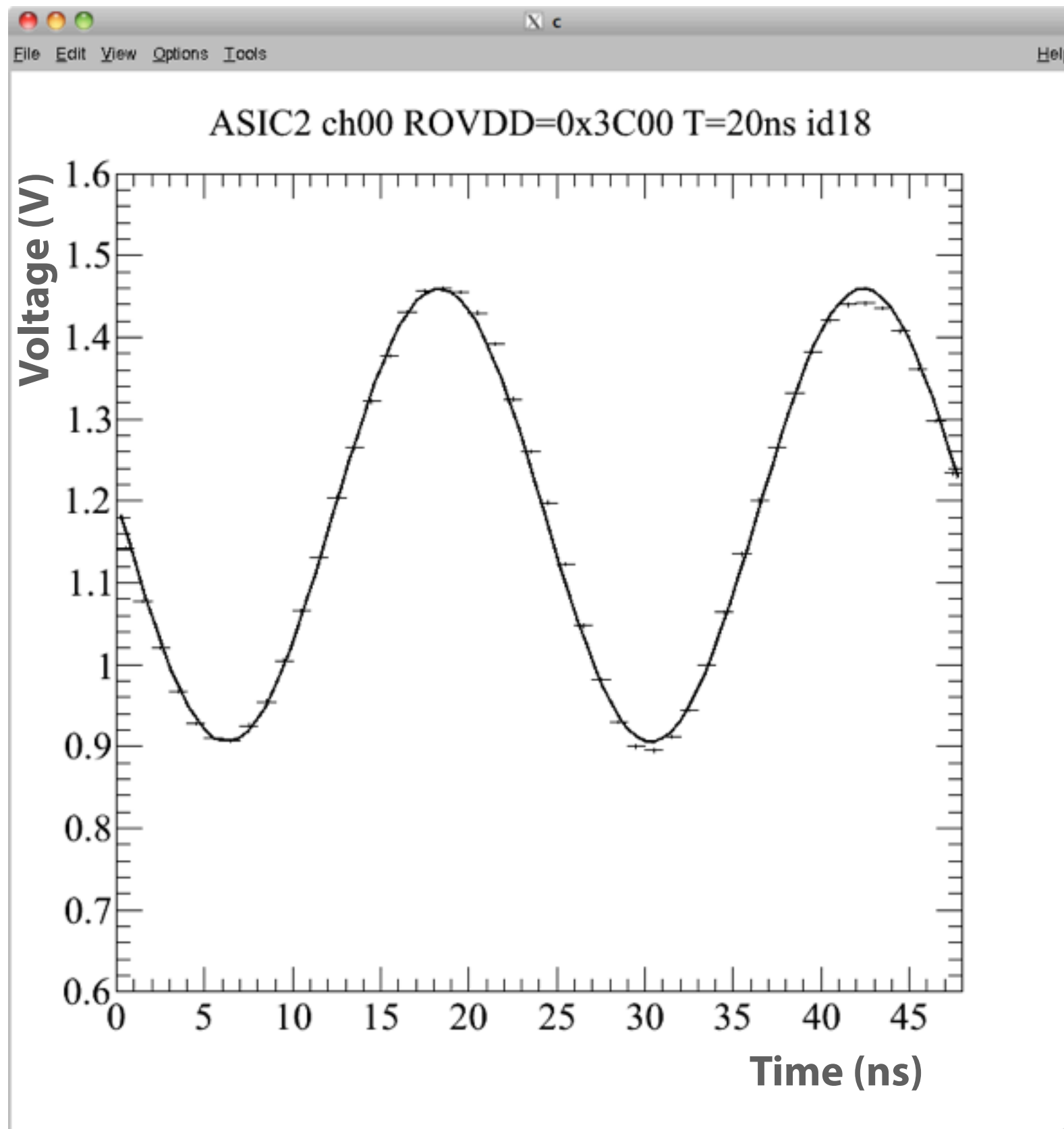


- Measure the ADC count in  $\sim 8$  mV step
- Dominant noise is common between Ch  $N$  and Ch  $N + 8$   $\sim 4$  (mV) ( $\sim 2$  ADC)
- Intrinsic noise  $\sim 1$  (mV) ( $\sim 0.5$  ADC)
- Fit  $V$  v.s. ADC with simple a function
- Fitting residual is  $\sim 0.5$  (mV) ( $\sim 0.25$  ADC)

$$V = p_0 + p_1 \text{ADC} + p_2 \text{ADC}^{1/2} + p_3 \text{ADC}^{1/3} + p_4 \text{ADC}^{1/4} + p_5 \text{ADC}^5$$



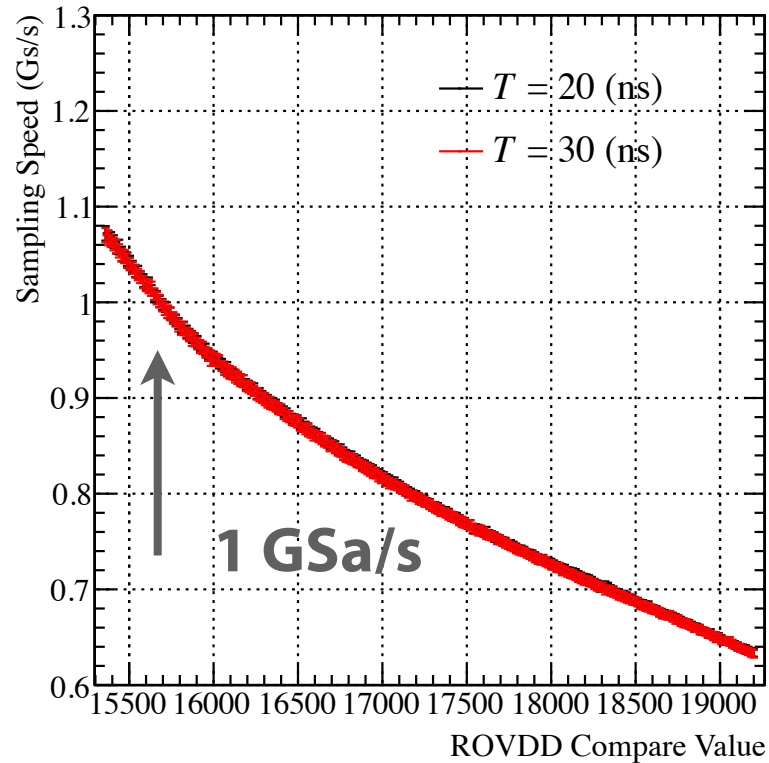
# Calibration of the Sampling Speed



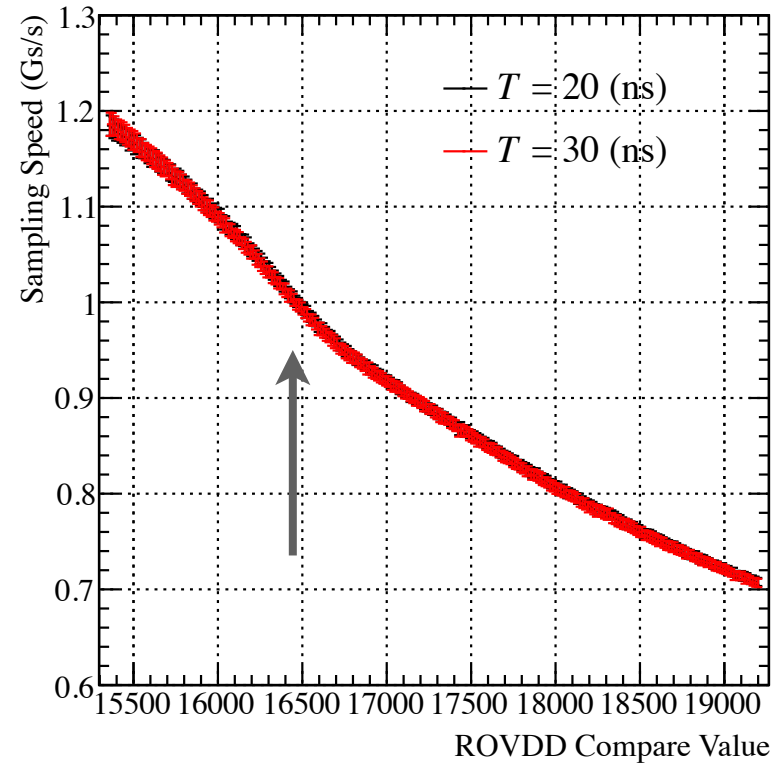
- ❑ Sampling speed is adjustable via control DC voltage
- ❑ Input 20 (ns) or 30 (ns) cycle sinusoid wave to the module
- ❑ Convert ADC counts to voltage using the transfer functions
- ❑ Fit the waveform and obtain the sampling speed

# Calibration of the Sampling Speed

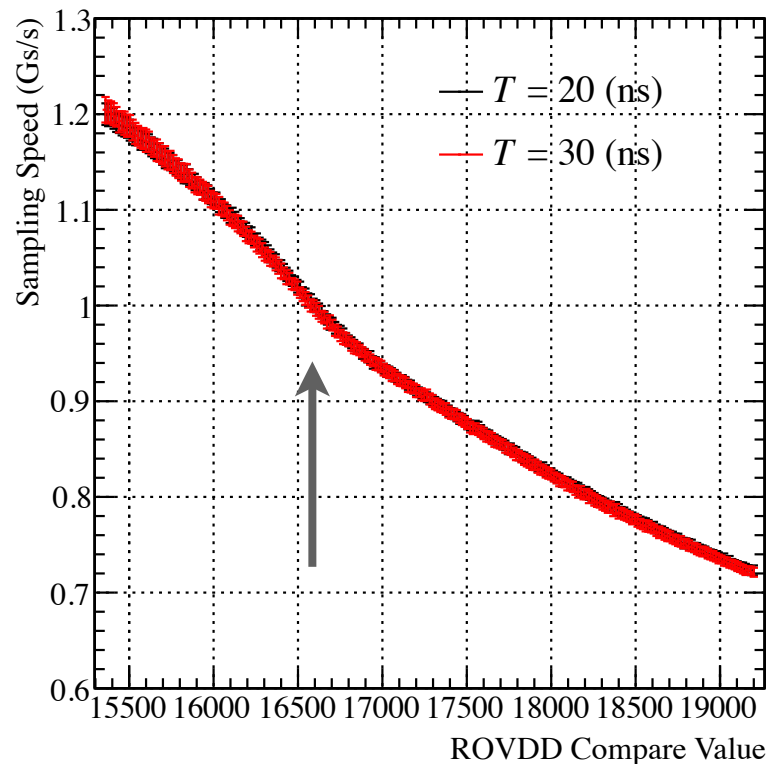
ASIC 1



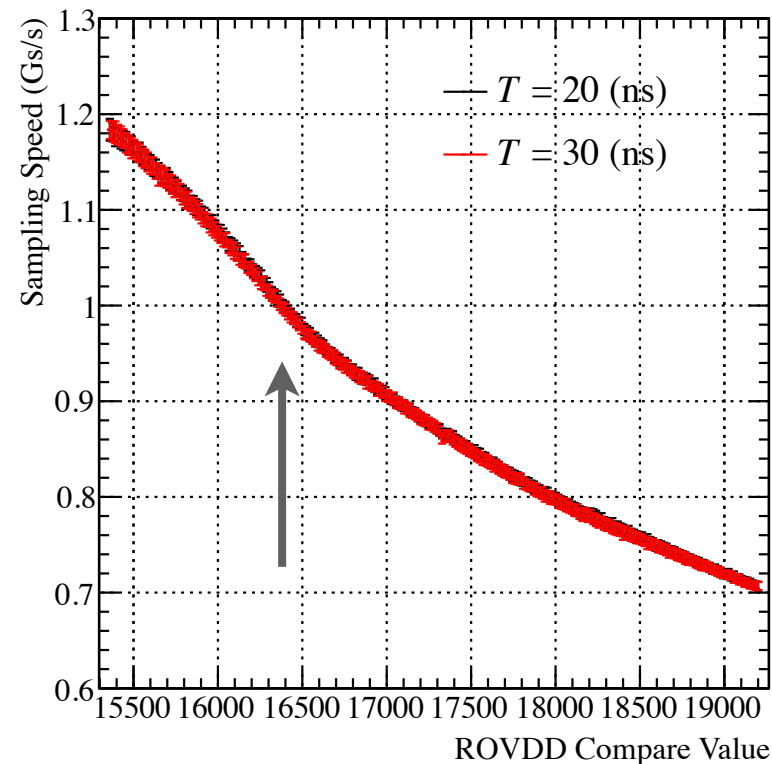
ASIC 2



ASIC 3



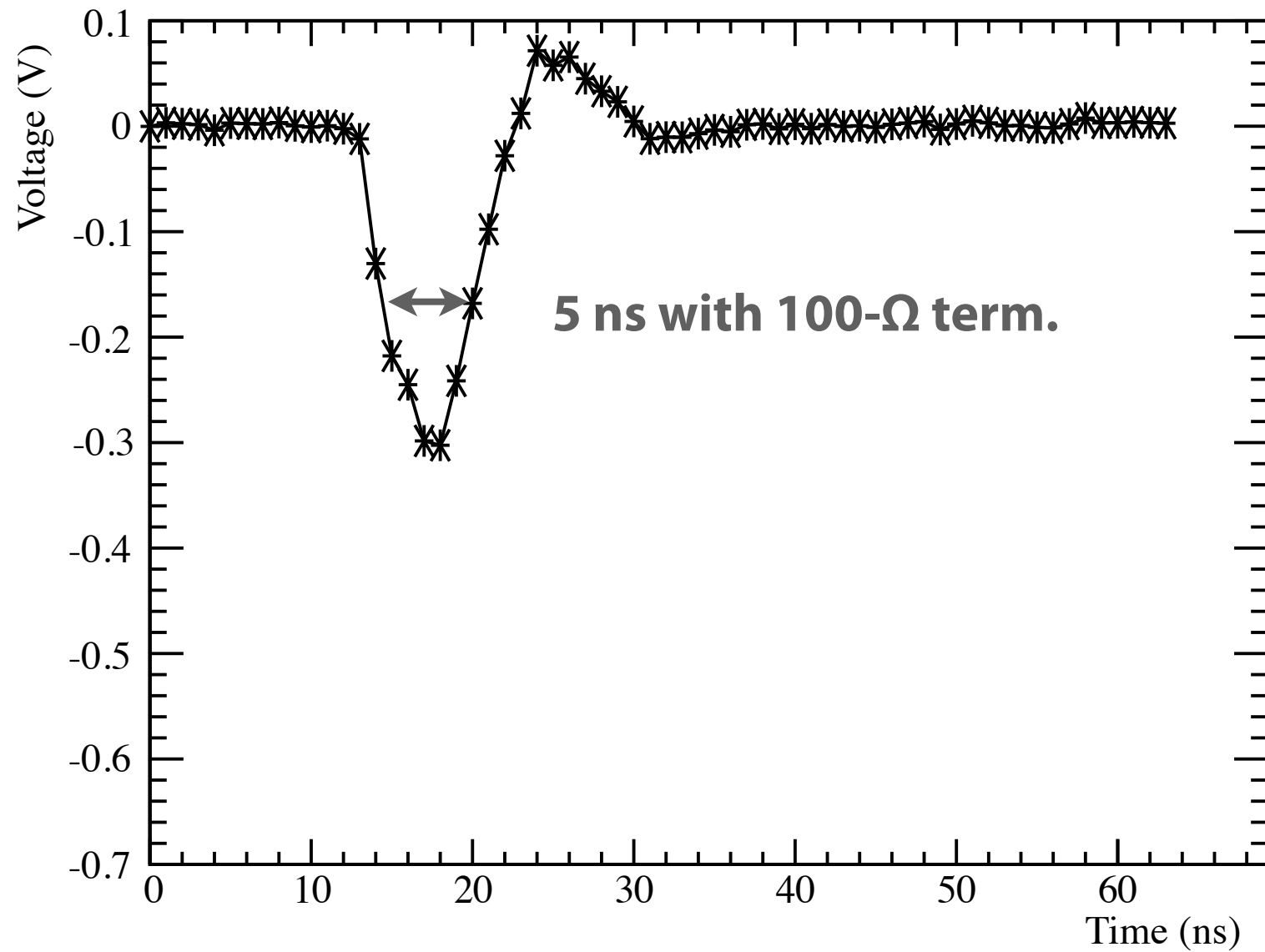
ASIC 4



- Sampling speed is adjustable via control DC voltage
- Input 20 (ns) or 30 (ns) cycle sinusoid wave to the module
- Convert ADC counts to voltage using the transfer functions
- Fit the waveform and obtain the sampling speed

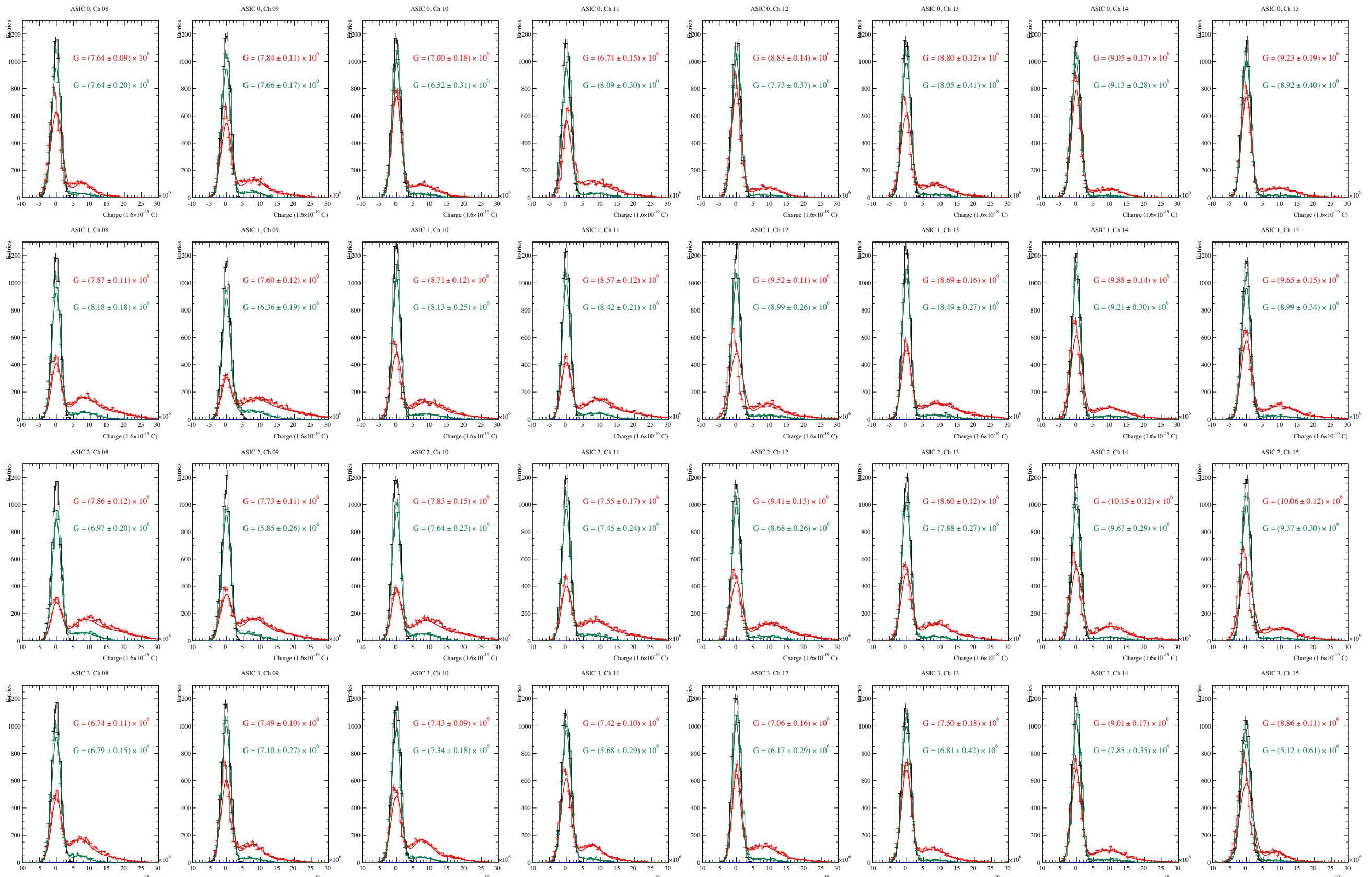


# Example of MAPMT Output



- Confirmed
- ▶ Waveform acquisition at 1 GSa/s
- ▶ Self trigger

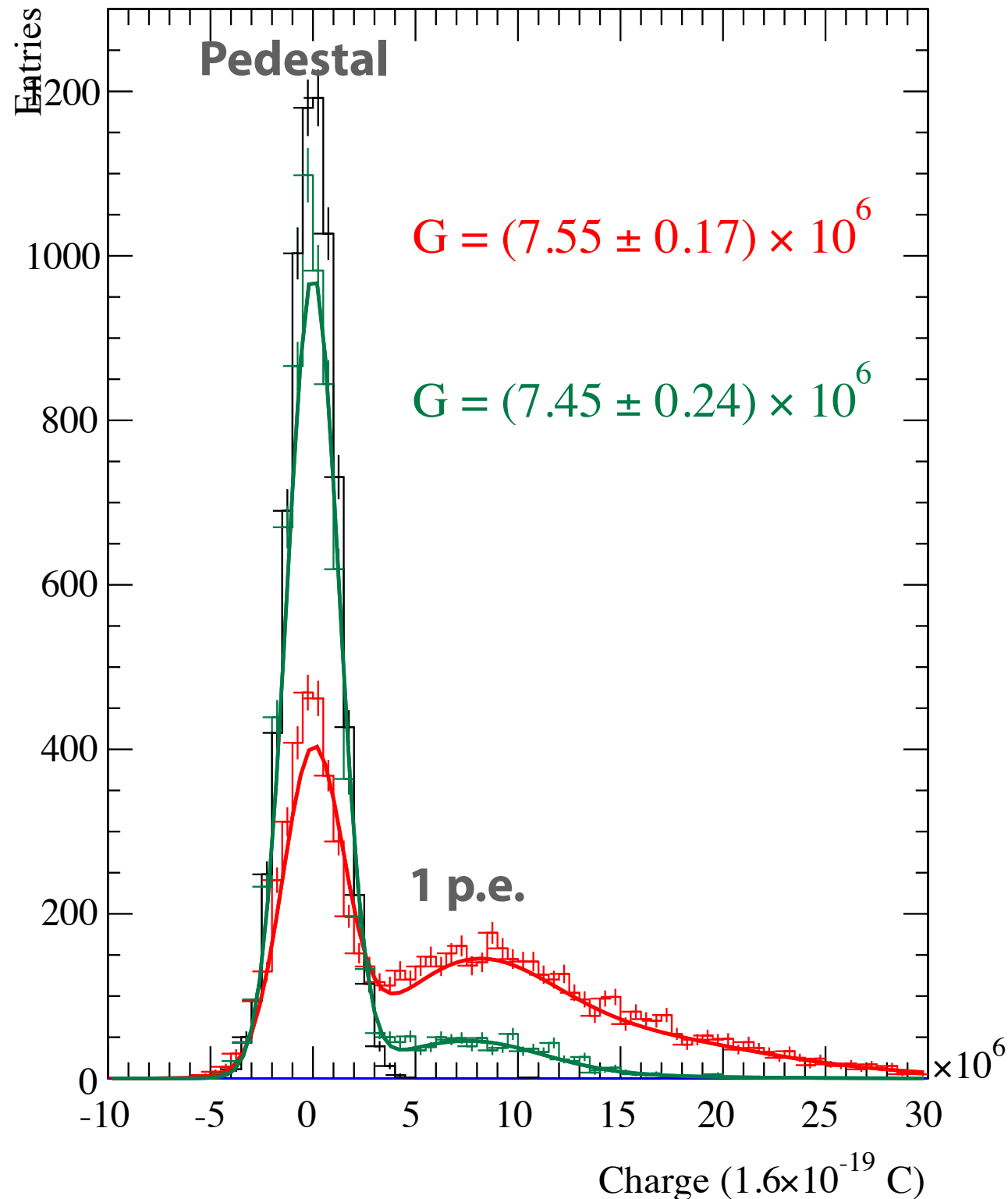
# 1 Photoelectron Distribution (32 Channels)





# 1 Photoelectron Distribution

ASIC 2, Ch 11

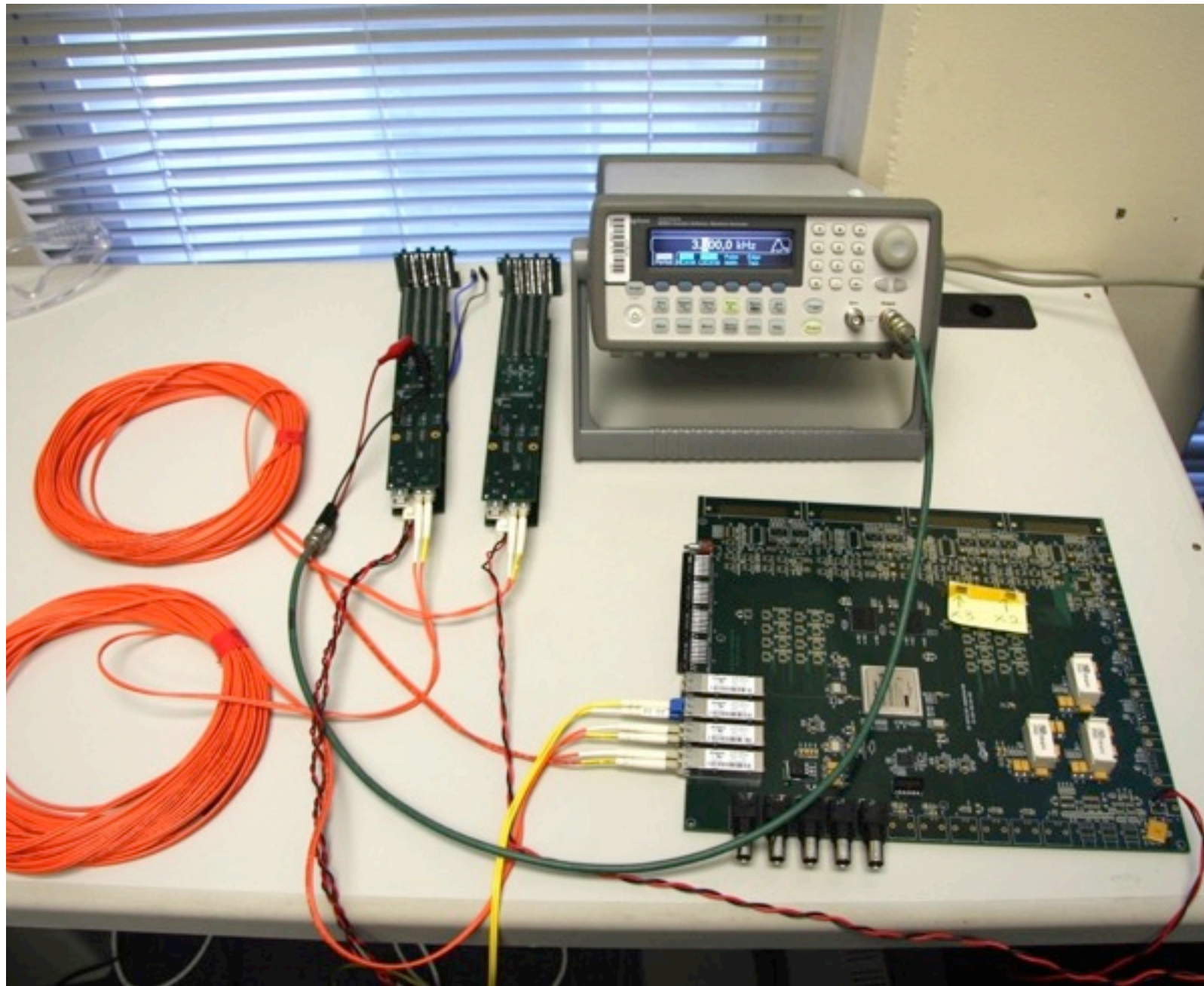


- 1 p.e. measurements using the pulsed LED and two different ND filters
- HV = -1050 (V)  
typical gain  $\sim 8 \times 10^6$
- Results of two different ND filters are shown

# Test Bench of Fiber Link

Camera Module × 2

Function Generator



Backend Board

- ❖ Prototype of a backplane board
- ❖ Receive data from multiple camera modules
- ❖ Broadcast commands from the board to modules
  - ▶ PC  $\Leftrightarrow$  Board  $\Leftrightarrow$  Modules
- ❖ > 6.6 kHz DAQ



# Summary

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- ❖ TARGET 1 has been developed for the Schwarzschild-Couder optical system
- ❖ Confirmed the basic functionalities of the TARGET camera module
- ❖ TARGET 2 has been already fabricated and delivered
- ❖ Some improvements are expected in TARGET 2